EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L51	0	(processor AND memory AND bus AND bit line AND remaining bit line AND first data pattern AND second data pattern AND read/write operation AND comparator AND delay adjustment AND delay locked loop circuit AND DLL)/clm.	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/05/22 20:07

5/22/08 8:10:18 PM
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